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September 8, 2003

Attorney Docket No.: 07977-256002

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22387 U.S. PTO  
10/656263  
09/08/03



## Mail Stop Patent Application

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Presented for filing is a new divisional patent application of:



BOSTON  
DALLAS  
DELAWARE  
NEW YORK  
SAN DIEGO  
SILICON VALLEY  
TWIN CITIES  
WASHINGTON, DC

Applicant: TAKESHI FUKADA

Title: SEMICONDUCTOR DEVICE AND METHOD OF  
MANUFACTURING THE SAME

The prior application is assigned of record to Semiconductor Energy Laboratory Co., Ltd., a Japan corporation, by virtue of an assignment submitted to the Patent and Trademark Office and recorded on July 12, 1999 at 010087/0482.

Enclosed are the following papers, including those required to receive a filing date under 37 CFR §1.53(b):

|               | <u>Pages</u> |
|---------------|--------------|
| Specification | 18           |
| Claims        | 7            |
| Abstract      | 1            |
| Declaration   | 3            |
| Drawing(s)    | 7            |

### Enclosures:

- Serial number card (green).
- Preliminary amendment, 12 pages.
- Transmittal of formal drawings, 1 page; and  
Formal drawings (Figs. 1A-7C, 6 pages).
- New disclosure information, including:  
Information disclosure statement, 1 page.  
PTO-1449, 2 pages.
- Change of correspondence address, 1 page.
- Copy of priority document – JP 10-131448 (1<sup>st</sup> page only), 1 sheet. Please  
enter in this application.
- Copy of recorded assignment, 4 pages. Please enter in this application.

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30/80/60  
15615 U.S. PTO  
This application is a divisional (and claims the benefit of priority under 35 USC 120) of U.S. application serial no. 09/296,042, filed April 21, 1999, which, under 35 USC 119, claims the benefit of a foreign priority application filed in Japan as serial number 10-131448 filed on April 24, 1998. The disclosures of the prior applications are considered part of (and are incorporated by reference in) the disclosure of this application.

|  |        |
|--|--------|
| Basic filing fee                             | \$750  |
| Total claims in excess of 20 times \$18      | \$720  |
| Independent claims in excess of 3 times \$84 | \$588  |
| Fee for multiple dependent claims            | \$0    |
| Total filing fee:                            | \$2058 |

A check for the filing fee is enclosed. Please apply any other required fees or any credits to deposit account 06-1050, referencing the attorney docket number shown above.

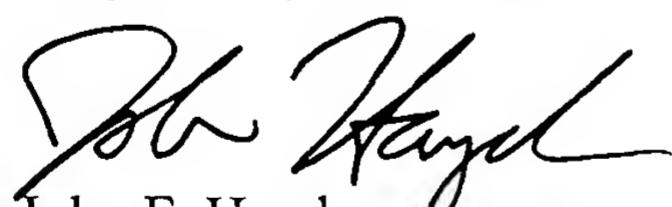
If this application is found to be incomplete, or if a telephone conference would otherwise be helpful, please call the undersigned at (202) 783-5070.

Kindly acknowledge receipt of this application by returning the enclosed postcard.

Please direct all correspondence to the following:

26171  
PTO Customer Number

Respectfully submitted,

  
John F. Hayden  
Reg. No. 37,640

**Enclosures**

JFH/aks

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Takeshi Fukada Art Unit : Unknown  
Serial No. : New Divisional Application Examiner : Unknown  
Filed : September 8, 2003  
Title : SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE  
SAME

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

## **TRANSMITTAL OF FORMAL DRAWINGS**

Please substitute the enclosed six (6) sheets of formal drawings (Figs. 1A-7C) for the corresponding drawings presently in the application.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: September 8, 2003

~~John F. Hayden~~  
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